

Wafer-Bow Control in DIBS-Deposited Thin-Film Optical Stacks to Improve SNSPD Performance

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Superconducting nanowire single-photon detectors (SNSPDs) are among the most powerful single-photon detection technologies, offering high efficiency, low noise, and excellent timing performance [1]. Further improvements can be achieved by integrating highly reflective mirrors—such as distributed Bragg reflectors (DBRs) beneath the superconducting nanowire to enhance photon absorption [2]. Previous attempts have been limited by excessive surface roughness of the mirror layers, which is incompatible with the stringent smoothness requirements of SNSPD fabrication.

Dual ion beam sputtering (DIBS) enables the deposition of dense, homogeneous thin films with extremely low surface roughness ($\text{RMS} < 0.1 \text{ nm}$) due to high particle energies and low process pressures. Using DIBS, multilayer $\text{Ta}_2\text{O}_5/\text{SiO}_2$ optical stacks are deposited on double-side polished 4-inch silicon wafers to form the DBR. However, the resulting film stress induces significant wafer bow, which must be reduced to below $2 \text{ }\mu\text{m}$ to allow reliable lithographic processing. In this work, we investigate several approaches to control and minimize wafer bow in DIBS-deposited optical stacks and compare experimental results with corresponding simulations.

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References

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